

APR 30 2007

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From: Daniel E. Ovanezian  
Date: 4/30/07 Time: \_\_\_\_\_  
Operator: \_\_\_\_\_ Matter: 008211.P004XC6  
Number of pages including cover sheet: 9

Message: Re: Patent No. 6,989,688 issued 1/24/06

Attached is:

1. Change of Correspondence Address, Power of Attorney, and Revocation of Previous Powers; and
2. Assignment documents that evidence the placement of title in the assignee for the above-referenced patent application.

CONFIDENTIALITY NOTE

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Attorney Docket No.: 001207.P004XC6

Patent

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: )  
 )  
 Benjamin S. Ting )  
 ) Examiner: Tan, Vibol  
 U.S. Patent No.: 6,989,688 )  
 ) Art Unit: 2819  
 Issue Date: 1/24/06 )  
 )  
 For: ARCHITECTURE AND INTERCONNECT )  
 SCHEME FOR PROGRAMMABLE )  
 LOGIC CIRCUITS )  
 \_\_\_\_\_ )

Commissioner for Patents  
 P.O. Box 1450  
 Alexandria, VA 22313-1450

**CHANGE OF CORRESPONDENCE ADDRESS,  
POWER OF ATTORNEY, AND REVOCATION OF PREVIOUS POWERS**

Please direct all future correspondence regarding the above-referenced patent application to Daniel E. Ovanezian, Reg. No. 41,236, Blakely, Sokoloff, Taylor, & Zafman LLP, 12400 Wilshire Boulevard, Seventh Floor, Los Angeles, California 90025, and direct all telephone calls to the same at (408) 720-8300.

Actel Corporation

(Name of Assignee)

("assignee"), a company organized under the laws of California, having a place of business at 2061 Stierlin Court, Mountain View, California 94043,  
 (Address)

hereby states that to the best of assignee's knowledge and belief it is the assignee of the entire right, title, and interest in and to the above-referenced patent application and represents that the undersigned is a representative authorized and empowered to sign on behalf of the assignee. Upon information and belief, the assignment documents that evidence the placement of title in the assignee are attached hereto.

Pursuant to 37 C.F.R. §§ 1.36 and 3.71, the assignee hereby revokes all powers

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of attorney previously given and appoints the practitioners associated with Customer Number 08791 as the assignee's respective patent attorneys and patent agents, with full power of substitution and revocation, to prosecute this application and to transact all business in the Patent and Trademark Office connected herewith.

Pursuant to 37 C.F.R. § 3.71, the assignee hereby states that prosecution of the above-referenced patent application is to be conducted to the exclusion of the inventor(s).

Assignee of Interest: Actel Corporation

Dated: 4/13/07 By:  (Type or Print)

Name: David Foster  
(Type or Print)

Title: Assistant Secretary  
(Type or Print)

Address of Assignee of Interest:

2061 Stierlin Court  
Mountain View, California 94043

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 4/30/07 By: 

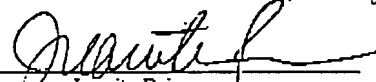
Name: Daniel E. Ovanezian  
(Type)

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**CERTIFICATE OF FACSIMILE TRANSMISSION**

I hereby certify that this correspondence is being transmitted by facsimile to the Patent and Trademark Office (Central Facsimile Number 571-273-8300) on 4/30/07

By:  4/30/07  
Juanita Briscoc Date

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## ASSIGNMENT OF PATENT RIGHTS BY BTR

This ASSIGNMENT OF PATENT RIGHTS, dated March 16, 2007 (this "Agreement"), is entered into by BTR, Inc., a Nevada corporation with a place of business at 20380 Town Center Lane, Suite 250, Cupertino, CA 95014 ("BTR" or "Assignor"), with and for the benefit of Actel Corporation, a California corporation with a place of business at 2061 Stierlin Court, Mountain View, CA 94043 ("Actel" or "Assignee").

**WHEREAS**, Assignor has agreed to sell and assign, and Assignee has agreed to buy and acquire all of Assignor's rights, title and interests in and to the patents and patent applications set forth in Exhibit A attached hereto (the "Assigned Patents").

**NOW, THEREFORE**, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, Assignor hereby assigns and transfers to Assignee any and all worldwide rights, title and interests Assignor holds, or may hold, in and to the Assigned Patents together with all rights derived therefrom, including but not limited to the right to sue for and collect damages for past, present and future infringement.

Assignor further agrees that, should additional or further documentation of the assignment be required for whatever reason, Assignor will, without further consideration, provide or execute such other information or documents as may be necessary upon Assignee's reasonable request.

This Agreement shall be binding on and shall inure to the benefit of, the Parties hereto and their respective successors and assigns. This Agreement will be governed by, and construed in accordance with, the internal laws of the State of California applicable to contracts executed and performed entirely therein, without regard to the principles of choice of law or conflicts or law of any jurisdiction. If any term or other provision of this Agreement is invalid, illegal or incapable of being enforced by any rule of law or public policy, all other conditions and provisions of this Agreement will nevertheless remain in full force and effect so long as the economic or legal substance of the transactions contemplated hereby is not affected in any manner materially adverse to any Party. Upon such determination that any term or other provision is invalid, illegal or incapable of being enforced, the Parties hereto will negotiate in good faith to modify this Agreement so as to effect the original intent of the Parties as closely as possible in a mutually acceptable manner in order that the transactions contemplated hereby be consummated as originally contemplated to the greatest extent possible.

IN WITNESS WHEREOF, Assignor has caused this Assignment of Patent Rights to be executed by its duly authorized representatives effective as of the date first written above.

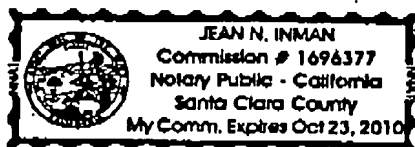
BTR, Inc.

By: *Richard P. Abraham*  
Name: Richard P. Abraham  
Title: President

STATE OF California  
COUNTY OF Santa Clara

On this 16<sup>th</sup> day of March, 2007, before me, a Notary Public in and for said State, personally appeared Richard P. Abraham personally known to me (or proved to me on the basis of satisfactory evidence) to be the person(s) whose names(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or entity upon behalf of which the person(s) acted, executed the instrument.

WITNESS, my hand and official seal.



*Jean N. Inman*  
Notary Public

**EXHIBIT A**  
**TO**  
**ASSIGNMENT OF PATENT RIGHTS BY BTR**

**ASSIGNED PATENTS**

Appl. No.	Issue Number	Filing Date	Title	Inventors
08/101,197	5,457,410 INVALIDATED/ ABANDONED	08-03-1993	ARCHITECTURE AND INTERCONNECT SCHEME FOR PROGRAMMABLE LOGIC CIRCUITS	Benjamin S. Ting Saratoga, CA
08/484,922	ABANDONED	06-07-1995		
09/034,769	6,433,580	03-02-1998		
09/955,589	6,507,217	09-13-2001		
10/269,364	6,703,861	10-11-2002		
10/692,880	7,017,136	10-23-2003		
11/299,248	PENDING	12-09-2005		
94922455.4	EU: 0712548	06-24-1994		
94922455.4	UK: 0712548	06-24-1994		
97111287.5	EU: 0806836	07-04-1997		
97111287.5	France: 0806836	07-04-1997		
97111287.5	Germany: 0806836	07-04-1997		
97111287.5	UK: 0806836	07-04-1997		
505821/95	Japan: PENDING	06-24-1994		
96-700569	Korea (South): 413881	02-03-1996		
US94/07187	Entered National Phase	06-24-1994		
94192983.3	China: 49812	06-24-1994		
9603530-8	Singapore: 44600	02-22-1996		
08/186,770	ABANDONED	01-25-1994	APPARATUS AND METHOD FOR PARTITIONING RESOURCES FOR INTERCONNECTIONS	Benjamin S. Ting Saratoga, CA
08/559,122	5,640,327 ABANDONED	02-09-1996		
95907365.1	ABANDONED	01-10-1995		
US95/00313	Entered National Phase	01-10-1995		
9605244-4	ABANDONED	02-03-1996		

Appl. No.	Issue Number	Filing Date	Title	Inventors
08/229,923	ABANDONED	04-14-1994	ARCHITECTURE AND INTERCONNECT SCHEME FOR PROGRAMMABLE LOGIC CIRCUITS	Benjamin S. Ting Saratoga, CA
08/534,500	ABANDONED	09-27-1995		
08/909,928	6,051,991 ABANDONED	08-12-1997		
09/482,149	6,462,578	01-12-2000		
10/117,875	6,597,196	04-05-2002		
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10/829,527	6,989,688	04-21-2004		
11/233,290	7,078,933	09-21-2005		
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95916402.1	EU: 00755588	04-14-1995		
95916402.1	Austria: 00755588	04-14-1995		
95916402.1	Belgium: 00755588	04-14-1995		
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95916402.1	Greece: 00755588	04-14-1995		
95916402.1	Ireland: 00755588	04-14-1995		
95916402.1	Italy: 00755588	04-14-1995		
95916402.1	Luxembourg: 00755588	04-14-1995		
95916402.1	Monaco: 00755588	04-14-1995		
95916402.1	Netherlands: 00755588	04-14-1995		
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95916402.1	Spain: 00755588	04-14-1995		
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95916402.1	UK: 00755588	04-14-1995		
95193431.7	China: ZL95193431.7	04-14-1995		
9611526-6	Singapore: 34648	04-14-1995		
527117/95	Japan: 3581152	10-14-1996		
96-705754	Korea (South): ABANDONED	10-14-1996		

Appl. No.	Issue Number	Filing Date	Title	Inventors
0119441.2	EU: 1162746	08-13-2001		
0119441.2	France: 1162746	08-13-2001		
0119441.2	Germany: 1162746	08-13-2001		
0119441.2	UK: 1162746	08-13-2001		
01118849.7	EU: Abandoned	08-13-2001		
05015294.1	EU: Abandoned	07-14-2005		
US95/04639	Entered National Phase	04-14-1995		
08/433,041	Abandoned	05-03-1995	SCALABLE MULTIPLE LEVEL TAB ORIENTED INTERCONNECT ARCHITECTURE	Benjamin S. Ting Saratoga, CA  Peter M. Pani Mountain View, CA
08/951,814	6,088,526 ABANDONED	10-14-1997		
09/377,304	6,300,793	08-18-1999		
96915392.3	EU: 0824791	04-30-1996		
96915392.3	UK: 0824791	04-30-1996		
02024873.8	EU: PENDING	11-08-2002		
96194985.6	China: ZL96194985.6	04-30-1996		
533406/96	Japan: 3727065	11-04-1997		
97-707796	Abandoned	11-04-1997		
US96/05964	Entered National Phase	04-30-1996		
8510228	Taiwan: NI-082978	05-01-1996		
9705151-0	Singapore: 46840	04-30-1996		
08/434,980	5,850,564 Abandoned	05-03-1995	FLOOR PLAN FOR SCALABLE MULTIPLE LEVEL TAB ORIENTED INTERCONNECT ARCHITECTURE	Benjamin S. Ting Saratoga, CA  Peter M. Pani Mountain View, CA
09/089,298	6,417,690	06-01-1998		
10/021,744	7,009,422	12-05-2001		
11/326,543	7,126,375	01-04-2006		
96920113.6	EU: 0824792	04-30-1996		
96920113.6	UK: 0824792	04-30-1996		
533412/96	Japan: 3684241	04-30-1996		
97-707797	Abandoned	11-03-1997		
96/05982	Entered National Phase	04-30-1996		
96194984.8	China: ZL 96194984.8	04-30-1996		



Appl. No.	Issue Number	Filing Date	Title	Inventors
85105227	Taiwan: NI-081900	05-01-1996	ARCHITECTURE AND INTERCONNECT FOR PROGRAMMABLE LOGIC CIRCUITS	Benjamin S. Ting Saratoga, CA  Peter M. Pani Mountain View, CA
9706178-2	Singapore: 53402	04-30-1996		
09/467,736	6,320,412	12-20-1999		
US00/35019	Abandoned	12-20-2000		